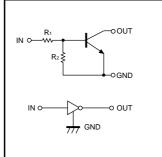
Digital transistors (built-in resistors)

DTC143XM / DTC143XE / DTC143XUA / DTC143XKA / DTC143XSA

Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see the equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 3) Only the on/off conditions need to be set for operation, making device design easy.

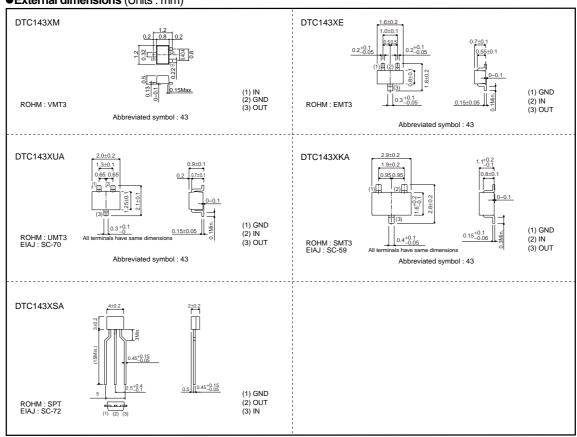
●Equivalent circuit



Structure

NPN digital transistor (with built-in resistors)

●External dimensions (Units : mm)



● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits(DTC143X□)					
raiaillelei		М	E	UA	KA	SA	Unit
Supply voltage	Vcc	50					V
Input voltage	Vin	−7~+20					V
Output current	lo	100					mA
	IC(Max.)	100					
Power dissipation	Pd	150		200		300	mW
Junction temperature	Tj	150					°C
Storage temperature	Tstg	-55~+150					°C

● Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions		
land callens	VI(off)	_	_	0.3	.,	Vcc=5V, Io=100μA		
Input voltage	V _{I(on)}	2.5	-	-	V	Vo=0.3V, Io=20mA		
Output voltage	Vo(on)	_	0.1	0.3	V	lo/l≔10mA/0.5mA		
Input current	lı	_	_	1.8	mA	Vi=5V		
Output current	IO(off)	_	_	0.5	μА	Vcc=50V, Vi=0V		
DC current gain	Gı	30	-	-	-	Vo=5V, Io=10mA		
Input resistance	R ₁	3.29	4.7	6.11	kΩ	-		
Resistance ratio	R2/R1	1.7	2.1	2.6	_	-		
Transition frequency	fτ	-	250	-	MHz	Vce=10V, Ie=-5mA, f=100MHz	*	

^{*} Transition frequency of the device

Packaging specifications

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	Package	VMT3	EMT3	UMT3	SMT3	SPT			
	Packaging type	Taping	Taping	Taping	Taping	Taping			
	Code	T2L	TL	T106	T146	TP			
Туре	Basic ordering unit (pieces)	8000	3000	3000	3000	5000			
DTC143XM		0	_	_	-	-			
DTC143XE		-	0	-	-	-			
DTC143XUA		-	-	0	-	-			
DTC143XKA		-	-	-	0	-			
DTC143XSA		-	-	-	=	0			

Electrical characteristic curves

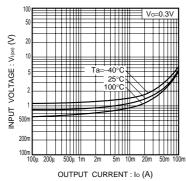


Fig.1 Input voltage vs. output current (ON characteristics)

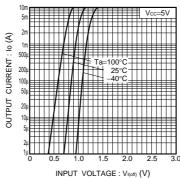


Fig.2 Output current vs. input voltage (OFF characteristics)

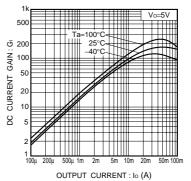


Fig.3 DC current gain vs. output current

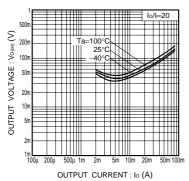


Fig.4 Output voltage vs. output current

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